

Application No.: 09/752,123

Docket No.: JCLA6706

**In The Drawings:**

Please substitute the attached clean drawing of Fig. 1 for the pending drawing of Fig. 1.

The amended portion is the addition of the several coprocessors and related bus connection.

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## REPLACEMENT SHEET

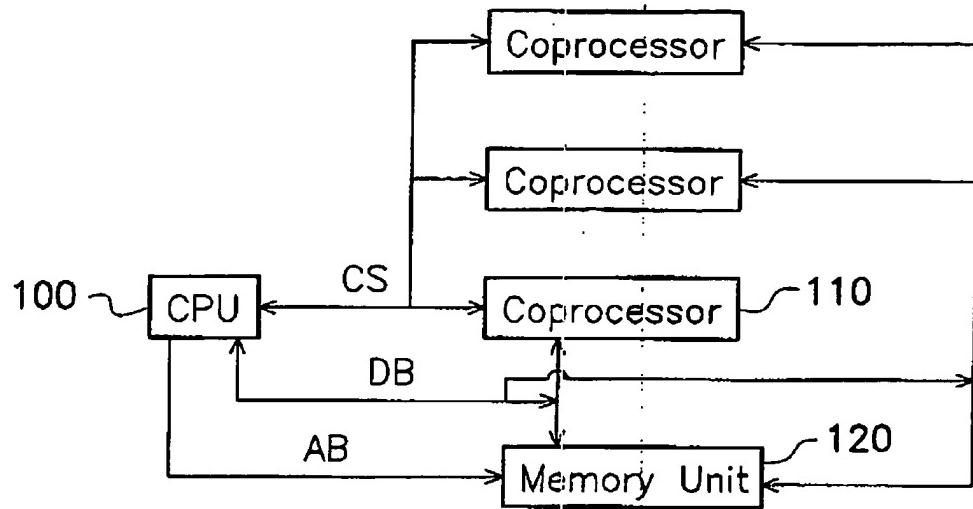


FIG. 1

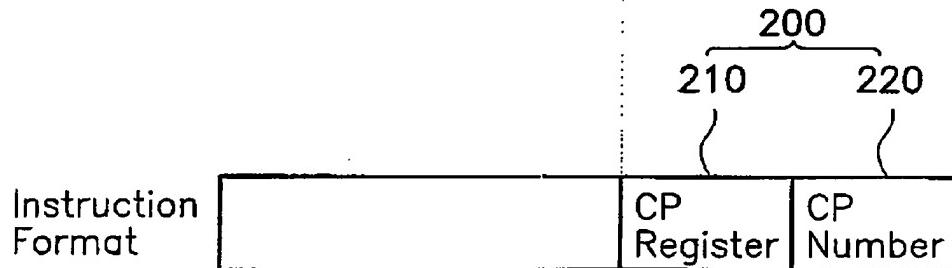


FIG. 2

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